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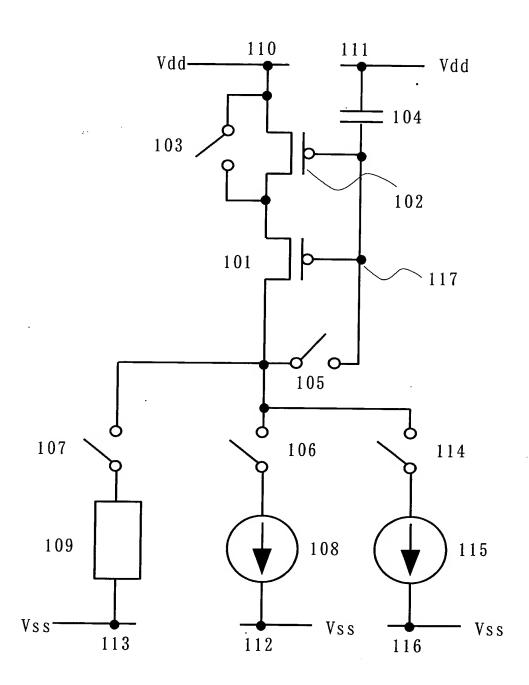


FIG. 1

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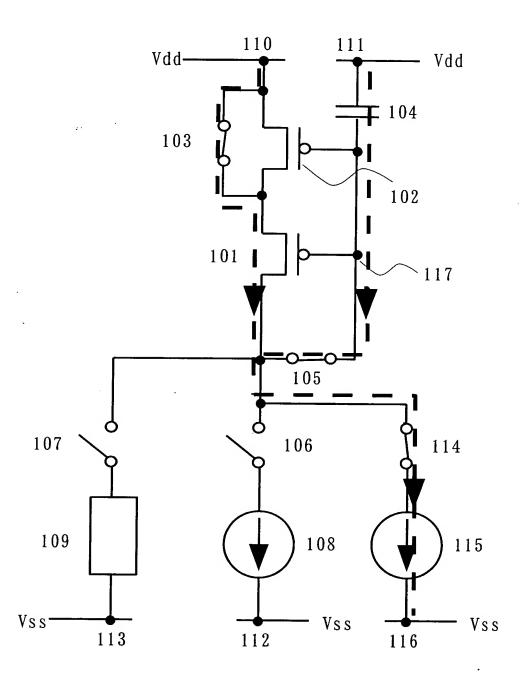


FIG. 2

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

**THEREOF** 

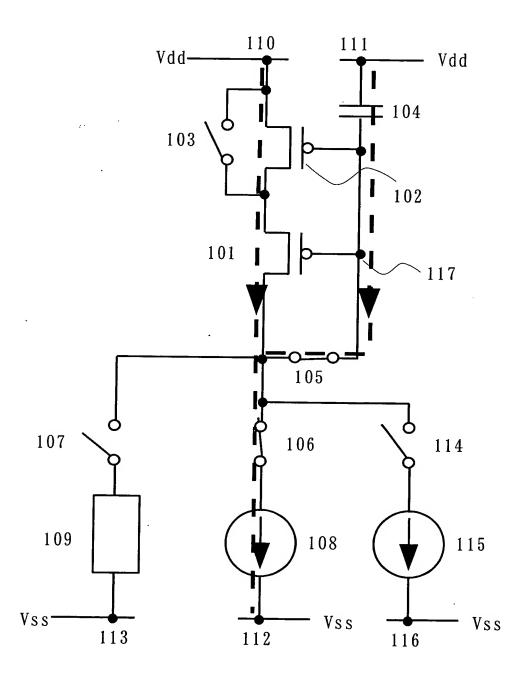


FIG. 3

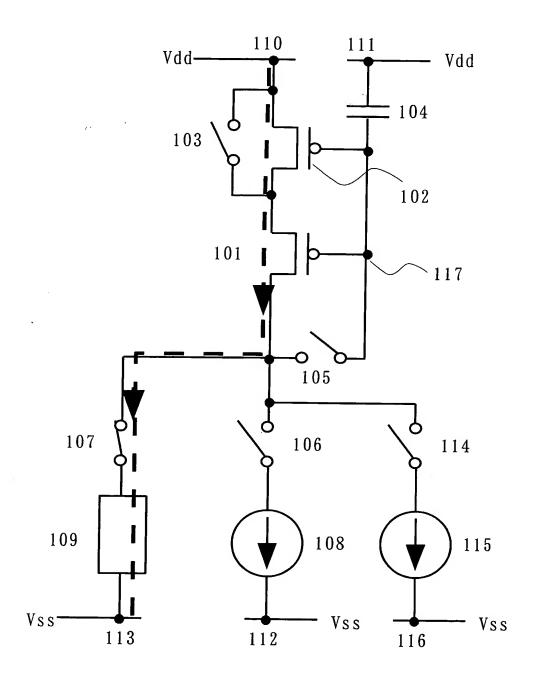
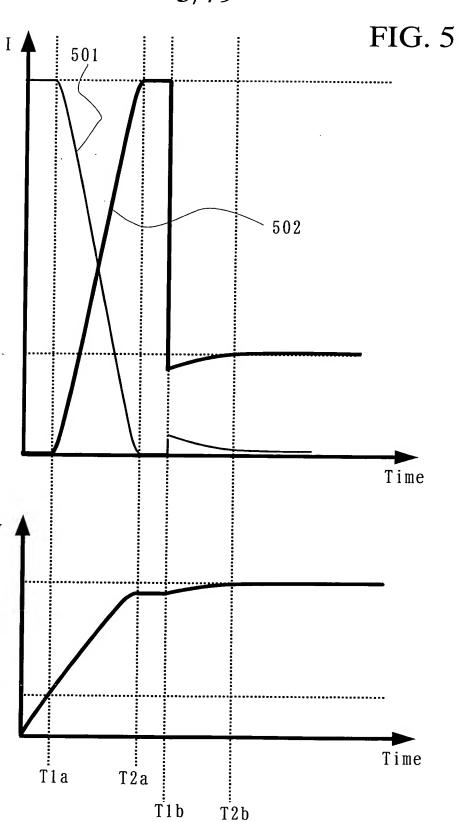


FIG. 4

Appln No.: 10/787,347 Page Applicant(s): Hajime Kimura SEMICONDUCTOR DEVICE AND DRIVING METHOD THEREOF





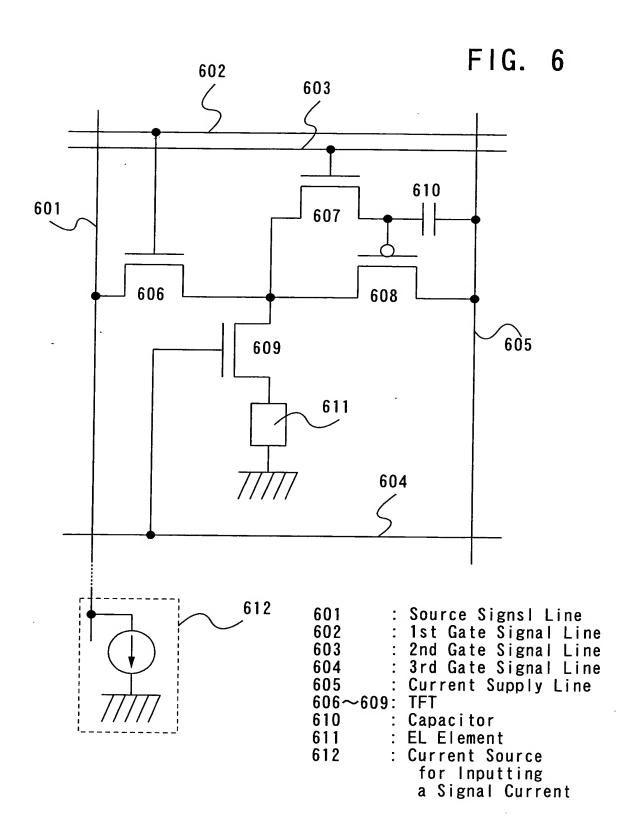
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SEMICONDUCTOR DEVICE AND DRIVING METHOD

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SEMICONDUCTOR DEVICE AND DRIVING METHOD

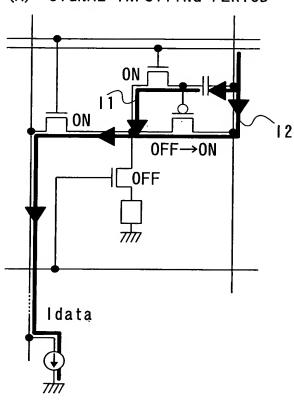
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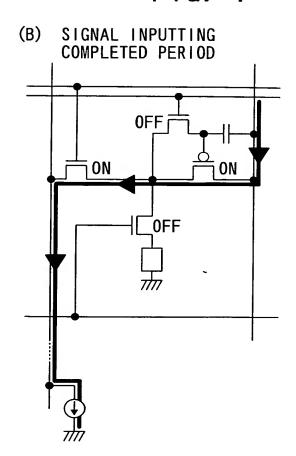
# 7/79

(D) Current

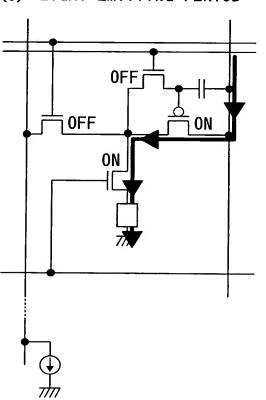
FIG. 7

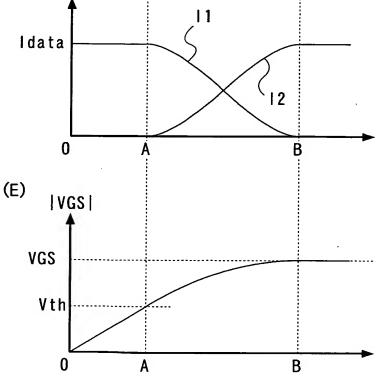
(A) SIGNAL INPUTTING PERIOD





(C) LIGHT EMITTING PERIOD





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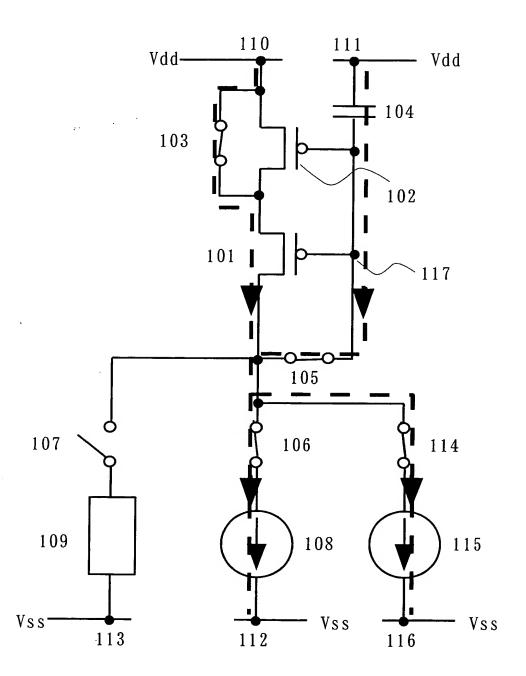


FIG. 8

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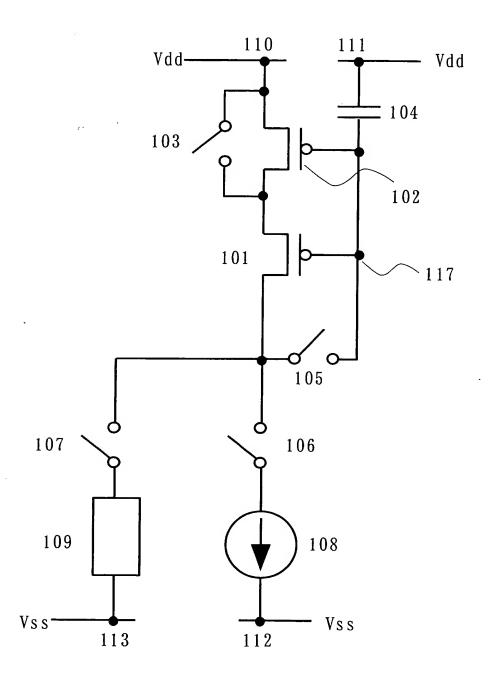


FIG. 9

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

**THEREOF** 

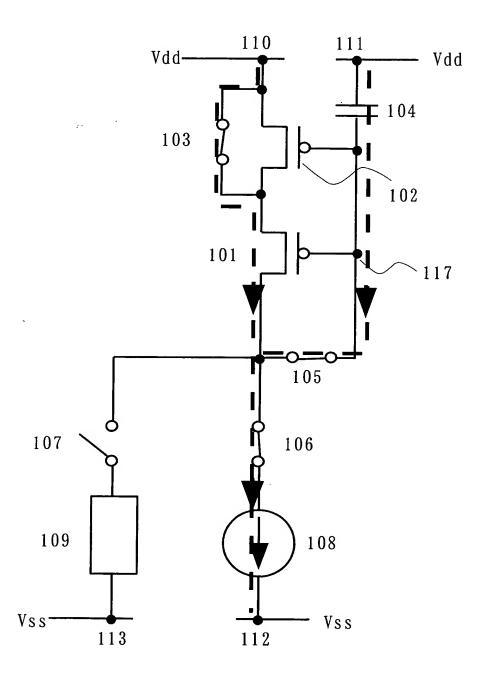


FIG. 10

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SEMICONDUCTOR DEVICE AND DRIVING METHOD
THEREOF

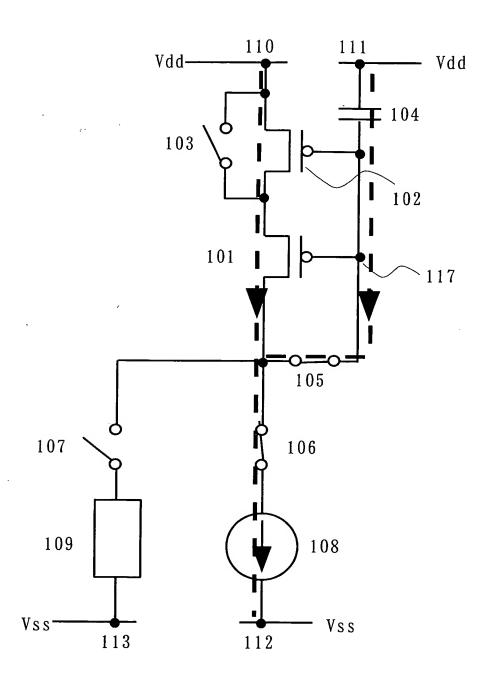


FIG. 11

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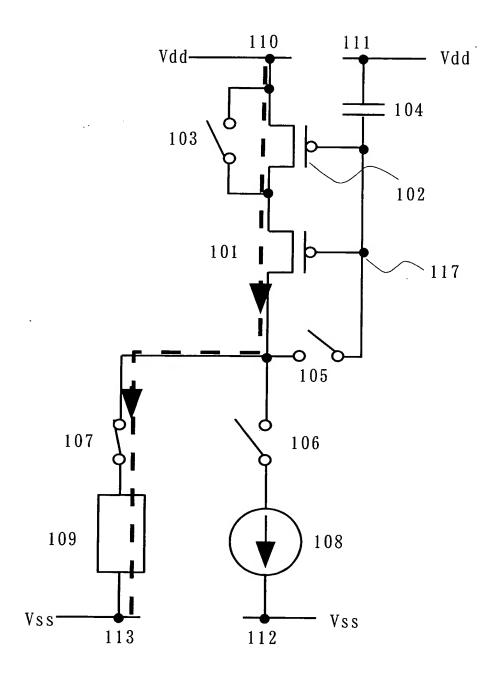


FIG. 12

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
THEREOF

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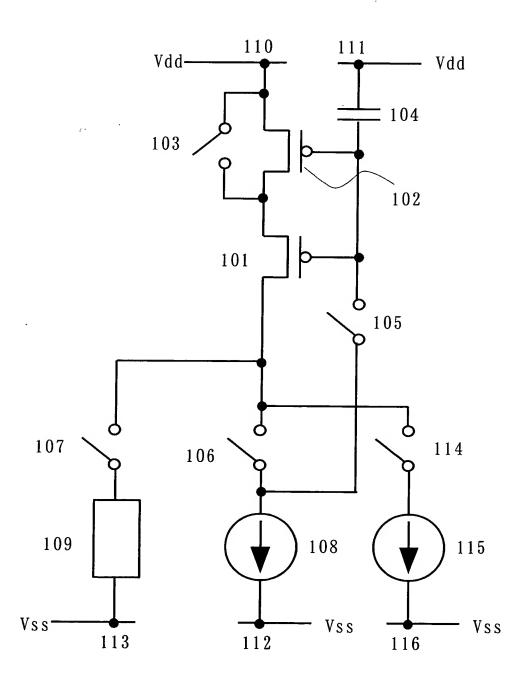


FIG. 13

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SEMICONDUCTOR DEVICE AND DRIVING METHOD Page 14 of 79

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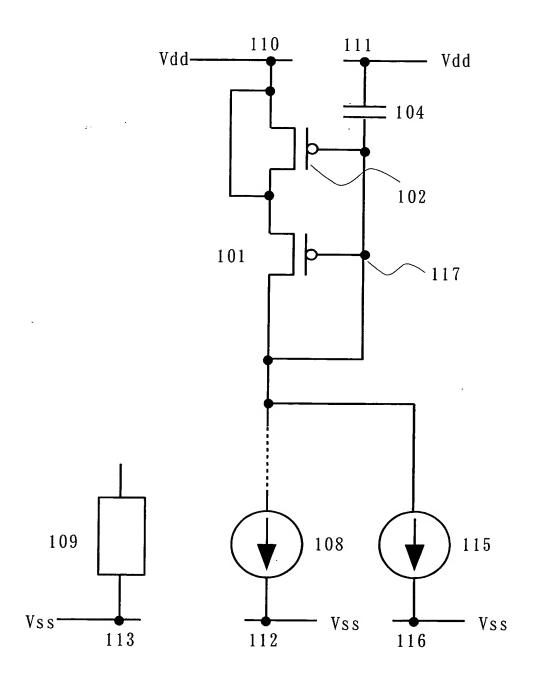


FIG. 14

Appln No.: 10/787,347 Page Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
THEREOF

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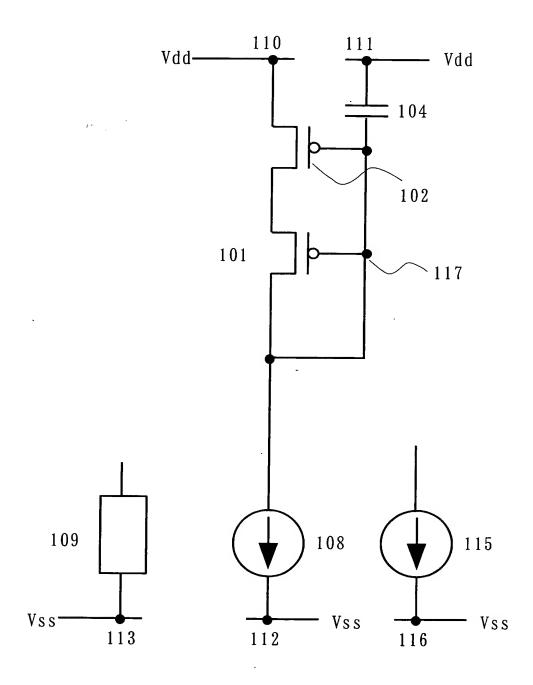


FIG. 15

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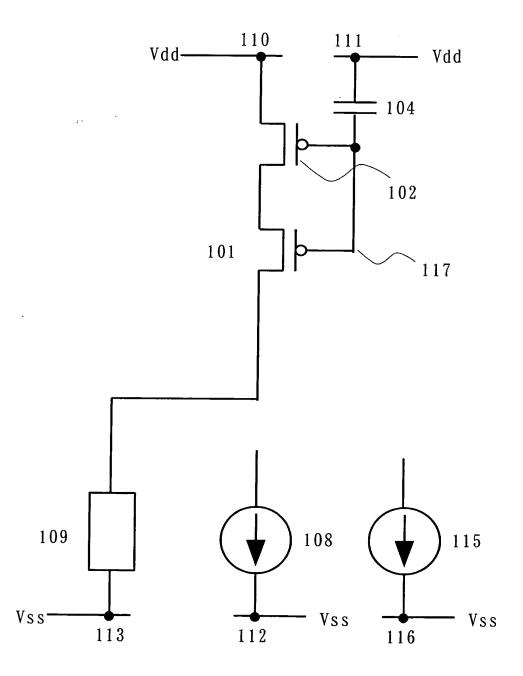


FIG. 16

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SEMICONDUCTOR DEVICE AND DRIVING METHOD



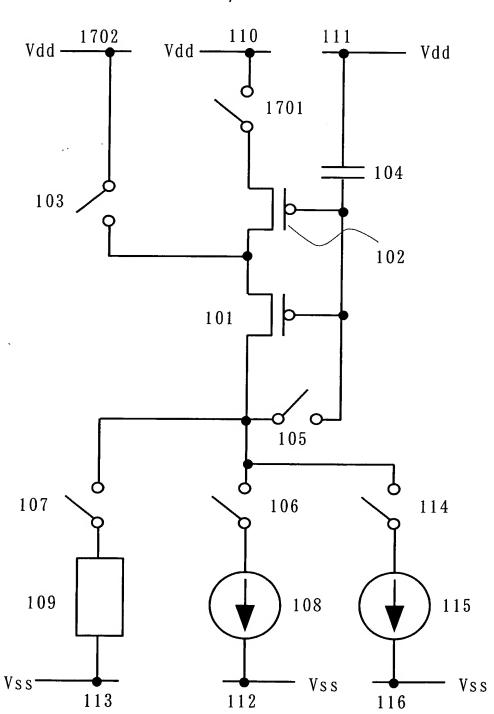


FIG. 17

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SEMICONDUCTOR DEVICE AND DRIVING METHOD Page 18 of 79

**THEREOF** 

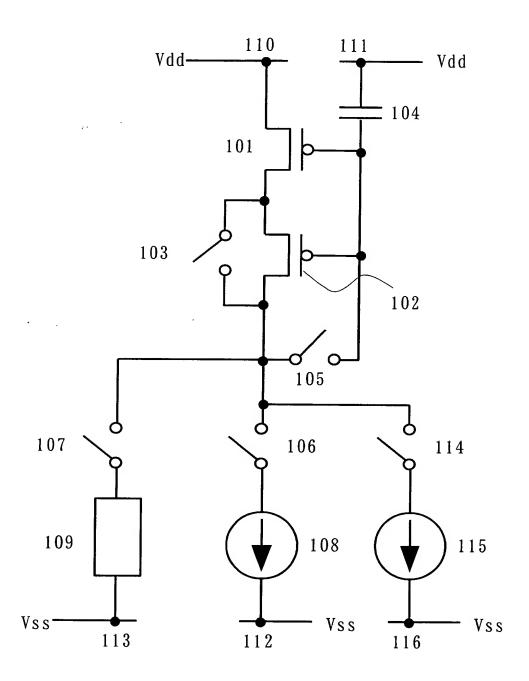


FIG. 18

Appln No.: 10/787,347 Page Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
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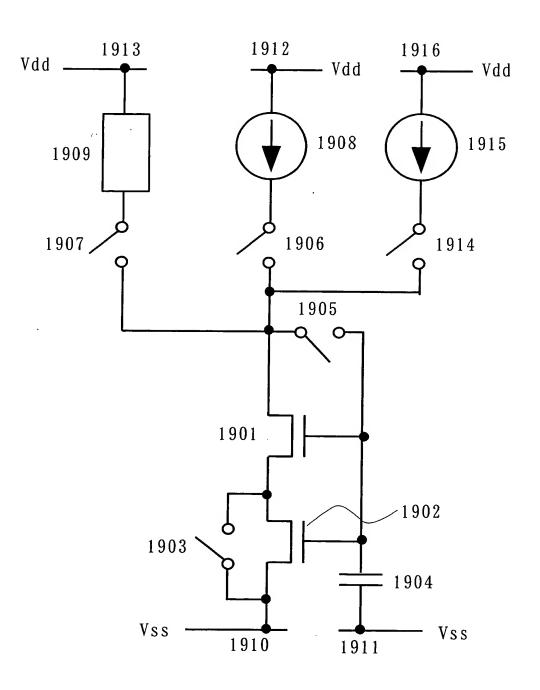


FIG. 19

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
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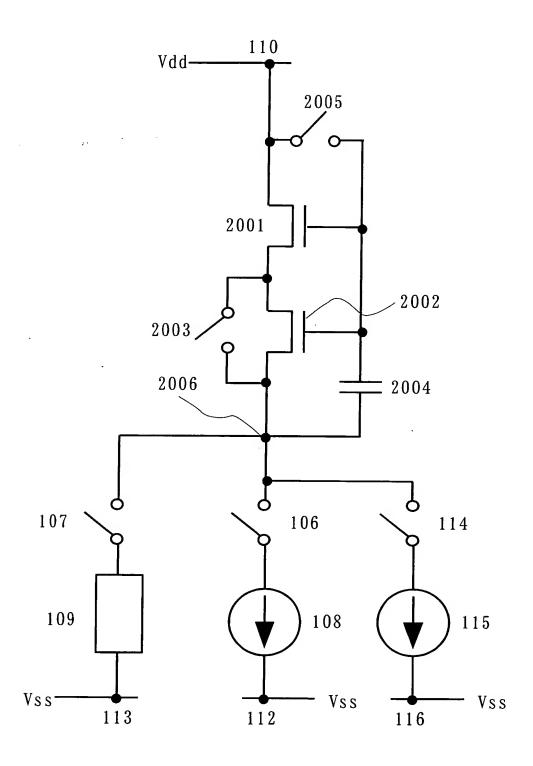


FIG. 20

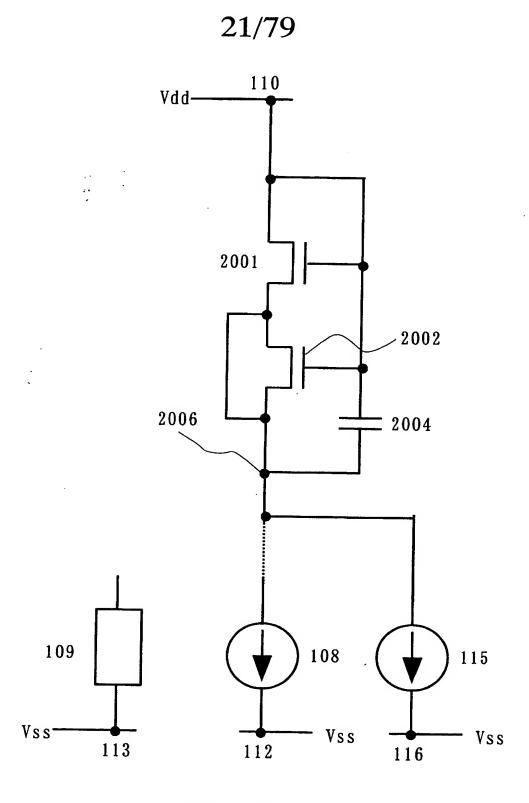


FIG. 21

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
THEREOF

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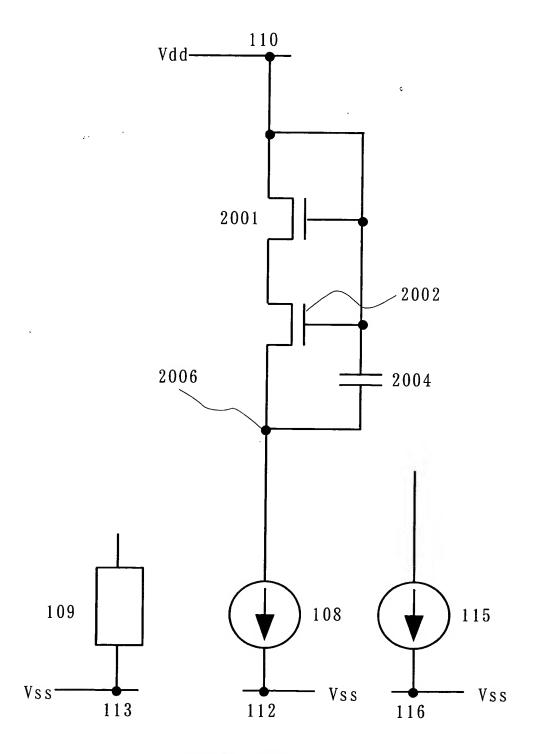


FIG. 22

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD



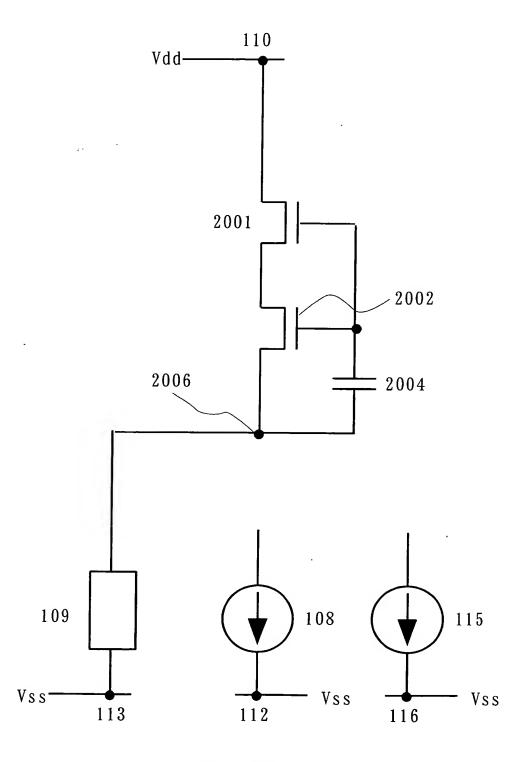
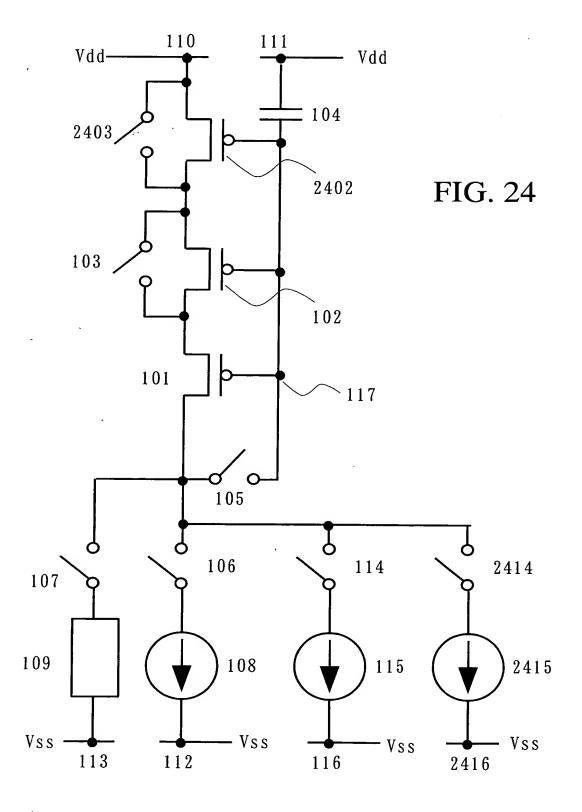


FIG. 23

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD Page 24 of 79

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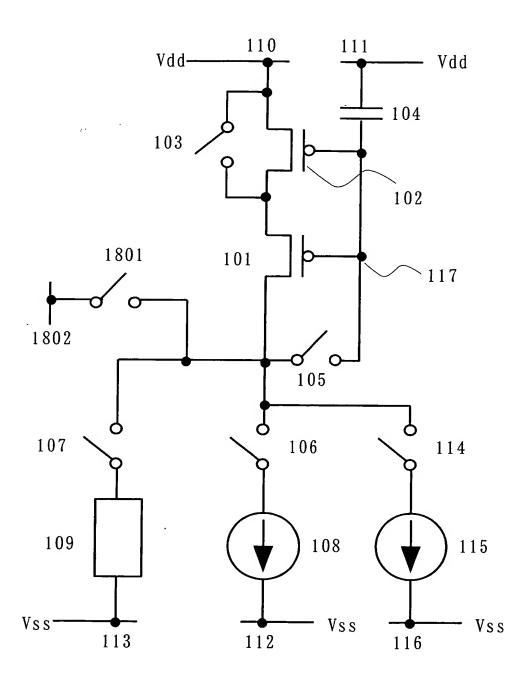


FIG. 25

Appln No.: 10/787,347 Page 2 Applicant(s): Hajime Kimura SEMICONDUCTOR DEVICE AND DRIVING METHOD

**THEREOF** 

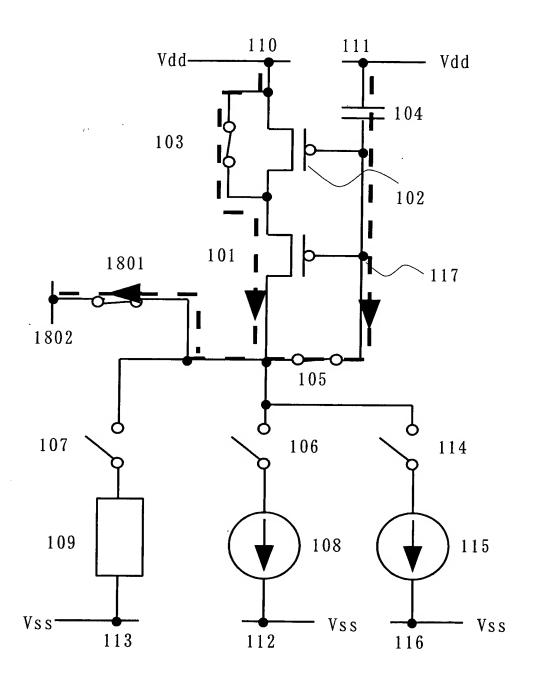


FIG. 26

Applin No.: 10/787,347 Page 2
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

**THEREOF** 

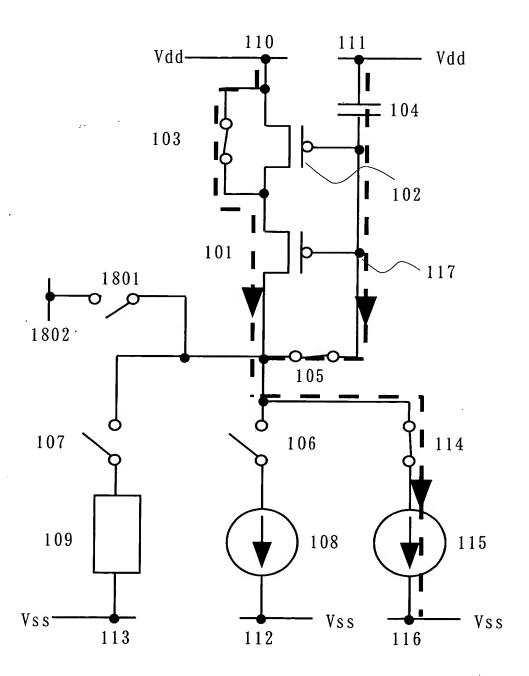


FIG. 27

Appln No.: 10/787,347 Page 2
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD Page 28 of 79

**THEREOF** 

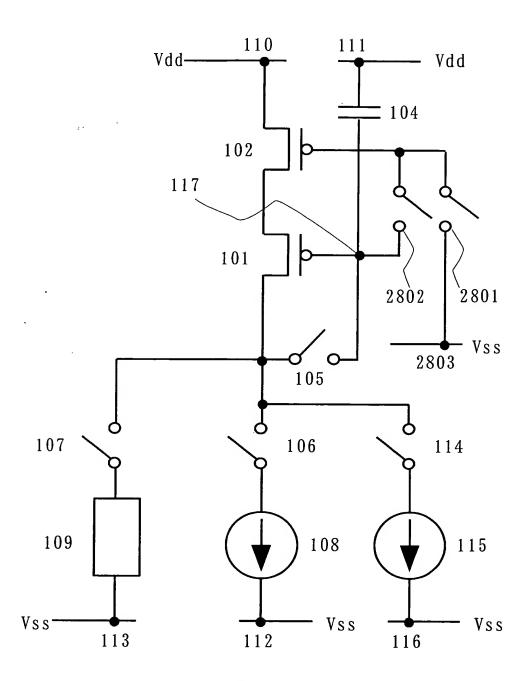


FIG. 28

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Appln No.: 10/787,347 Page 2
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

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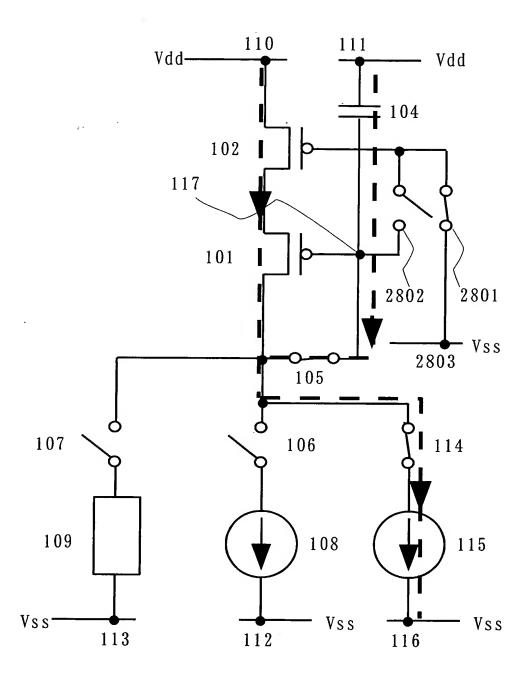


FIG. 29

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Appln No.: 10/787,347 Page 3
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
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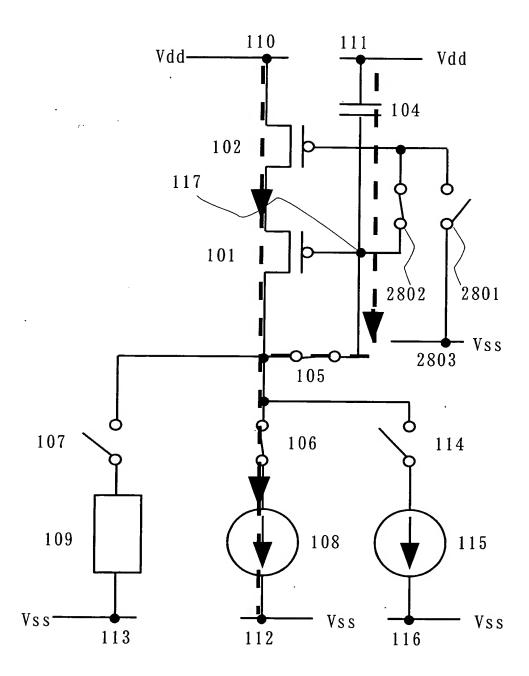


FIG. 30

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Appln No.: 10/787,347 Page 3
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
THEREOF

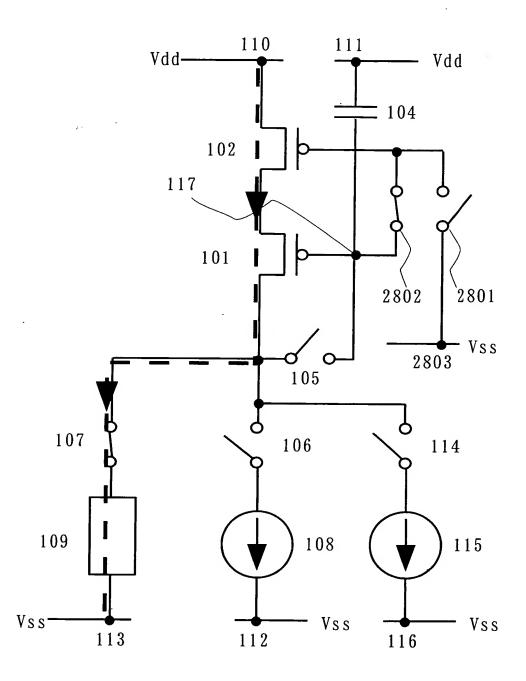


FIG. 31

Appln No.: 10/787,347 Page 3
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
THEREOF

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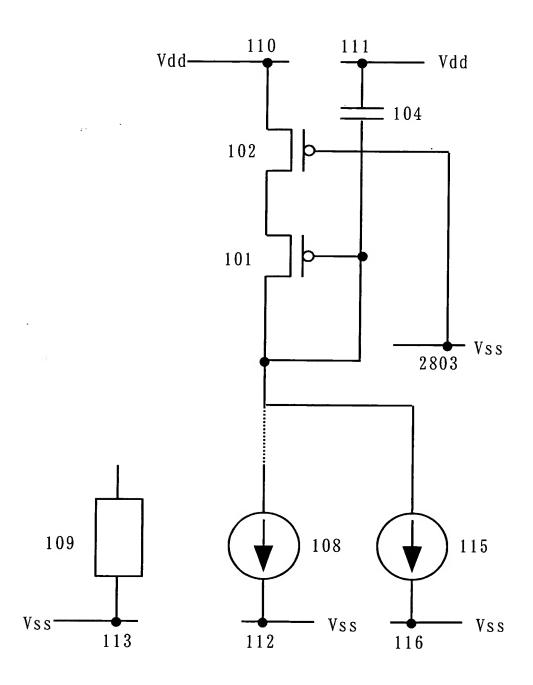


FIG. 32

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

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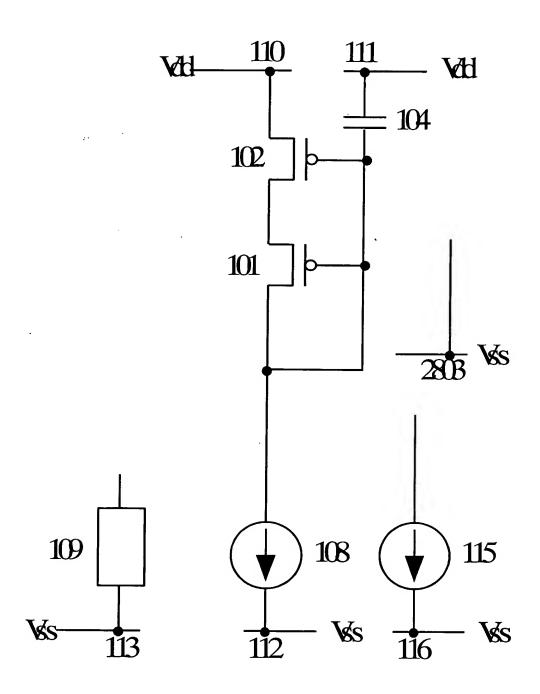


FIG. 33

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Applicant(s): Hajime Kimura
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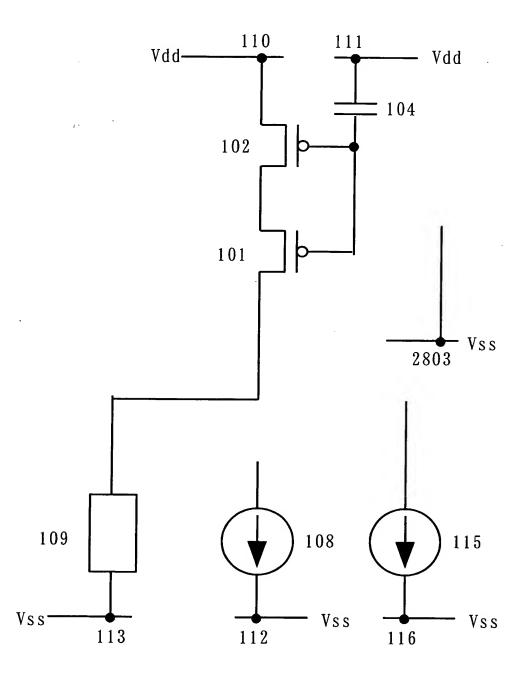


FIG. 34

**THEREOF** 

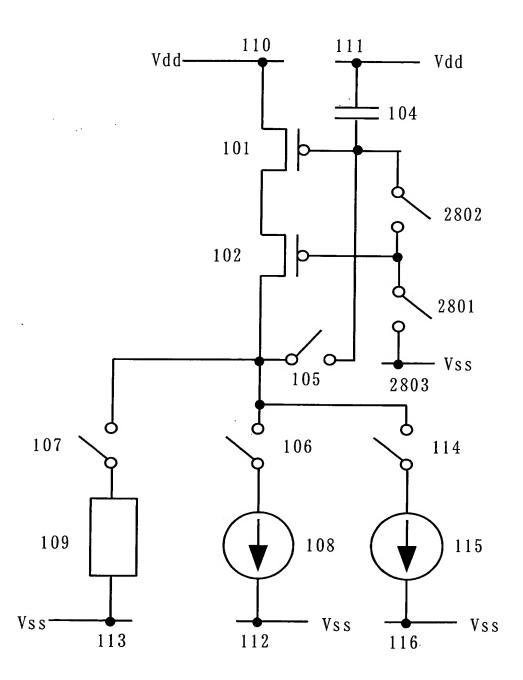


FIG. 35

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

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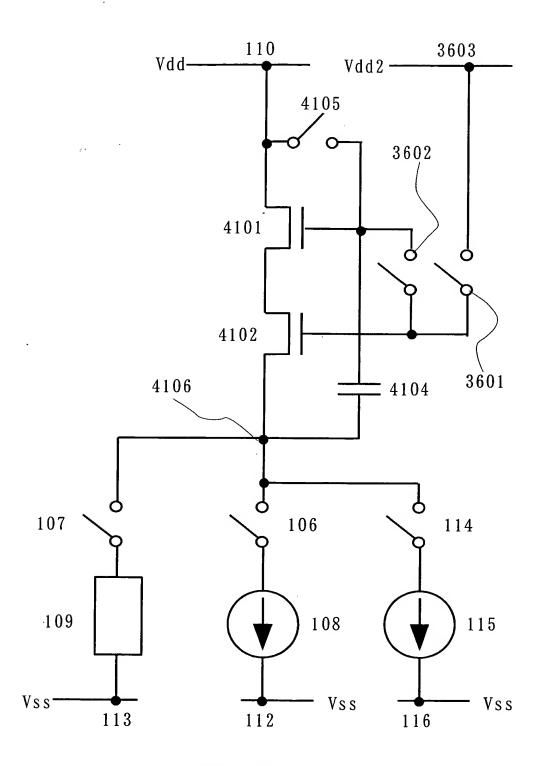


FIG. 36

Appln No.: 10/787,347 Page 3
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

**THEREOF** 

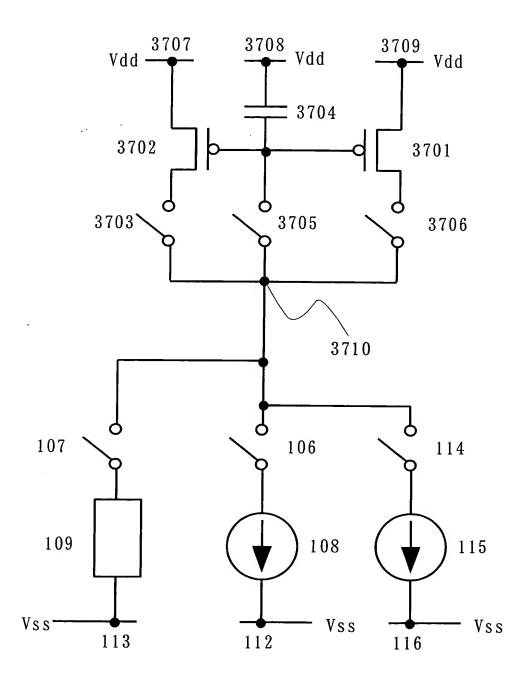


FIG. 37

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Applicant(s): Hajime Kimura
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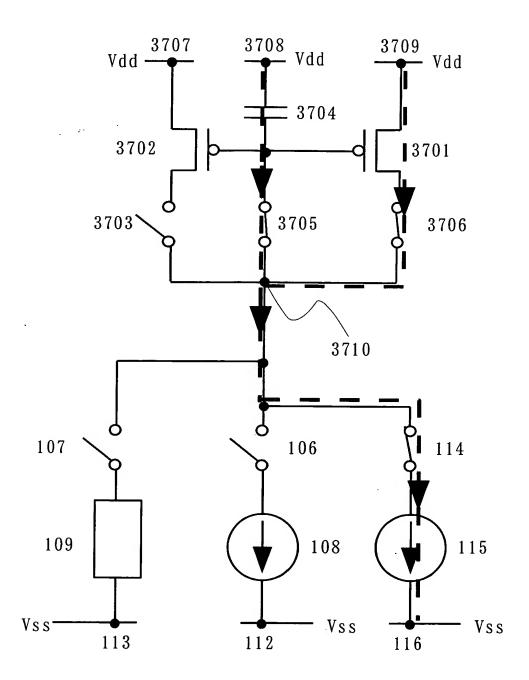


FIG. 38

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
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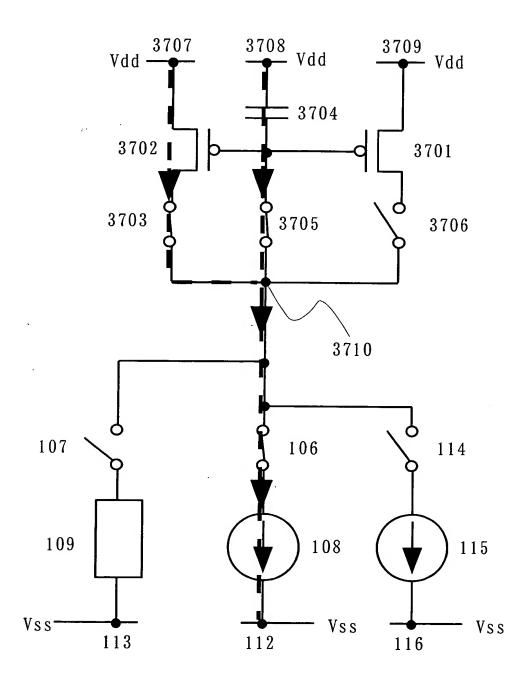


FIG. 39

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
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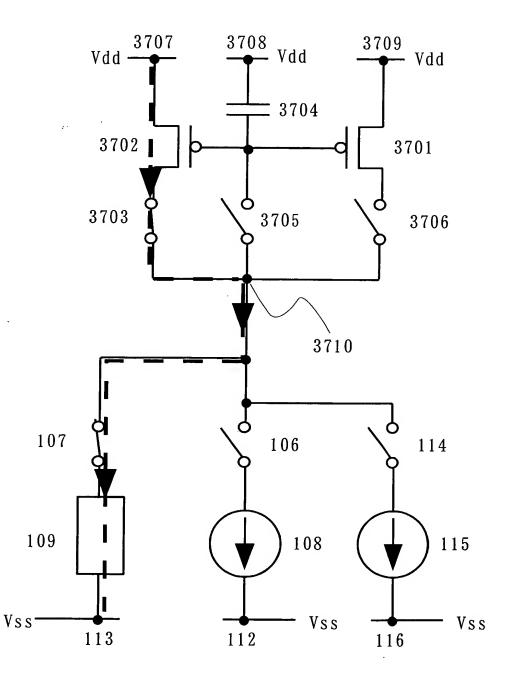


FIG. 40

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
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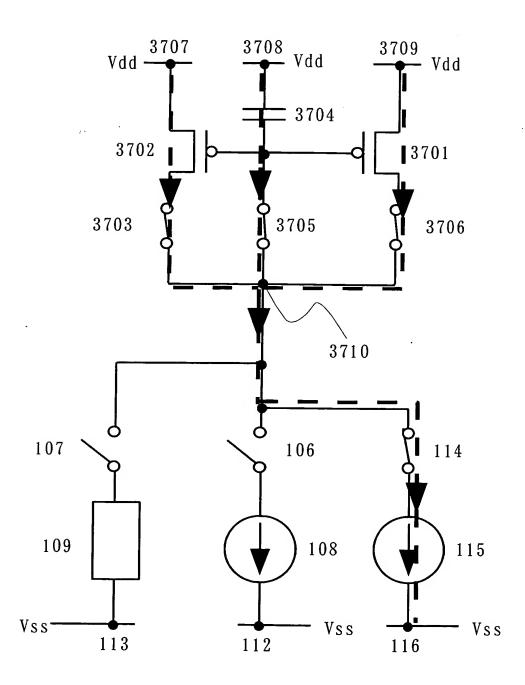


FIG. 41

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
THEREOF

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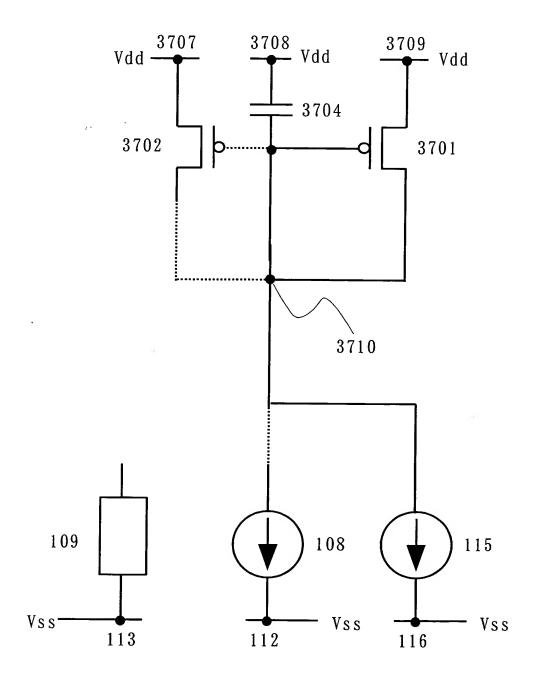


FIG. 42

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

**THEREOF** 

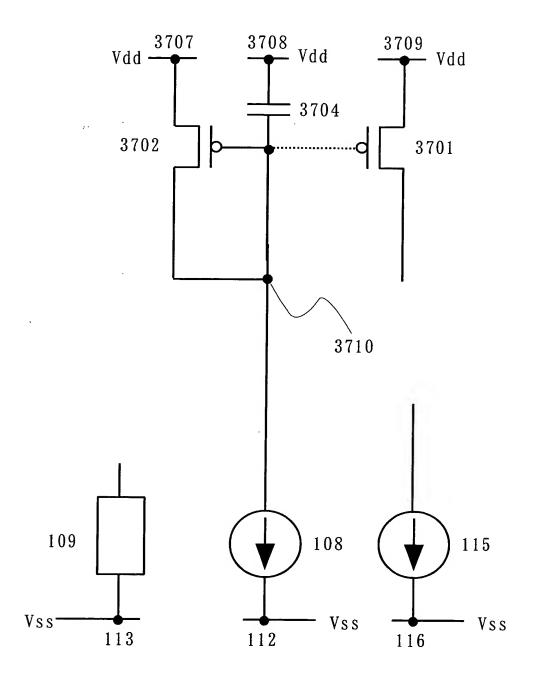


FIG. 43

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Appln No.: 10/787,347 Page 4
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
THEREOF

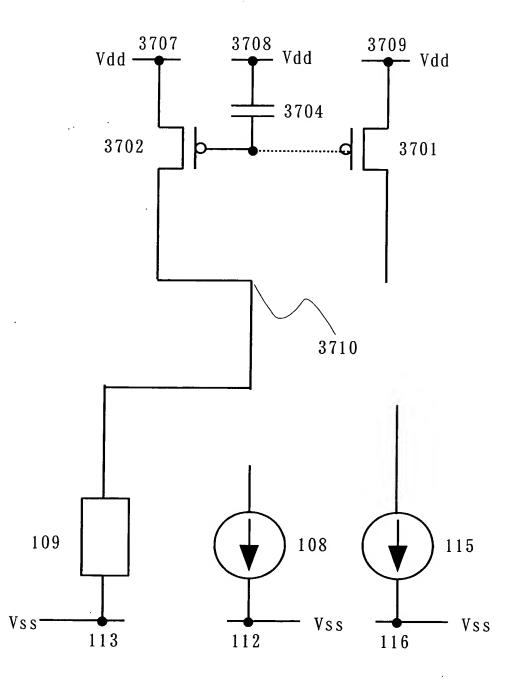


FIG. 44

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD
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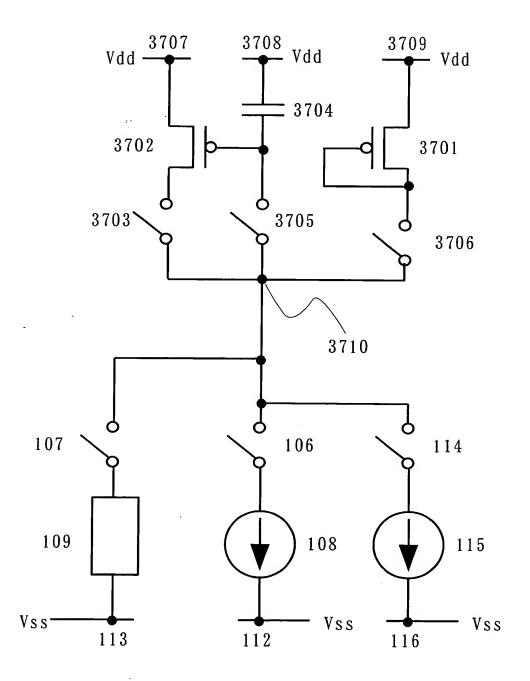


FIG. 45

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

**THEREOF** 

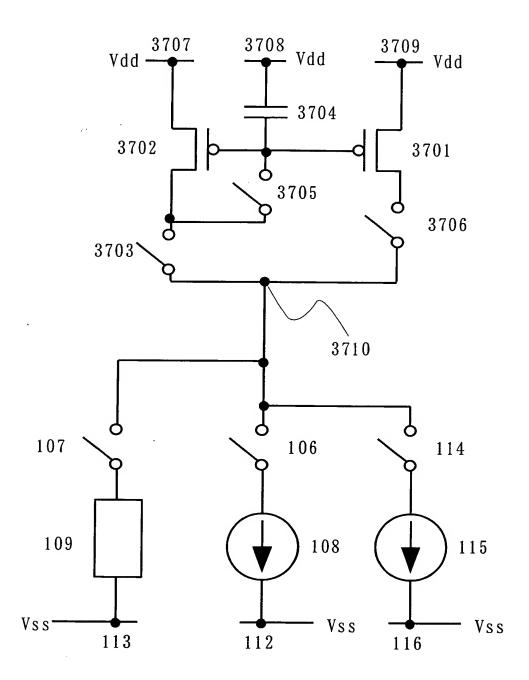


FIG. 46

Appln No.: 10/787,347 Page 4
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD **THEREOF** 

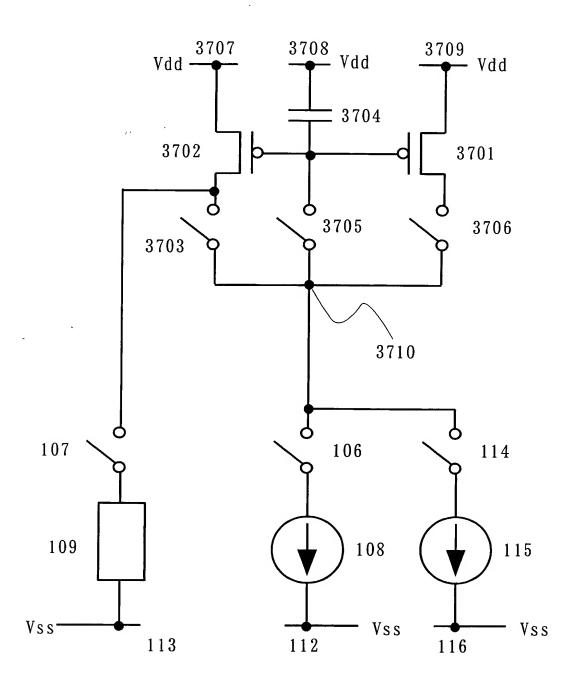


FIG. 47

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

**THEREOF** 

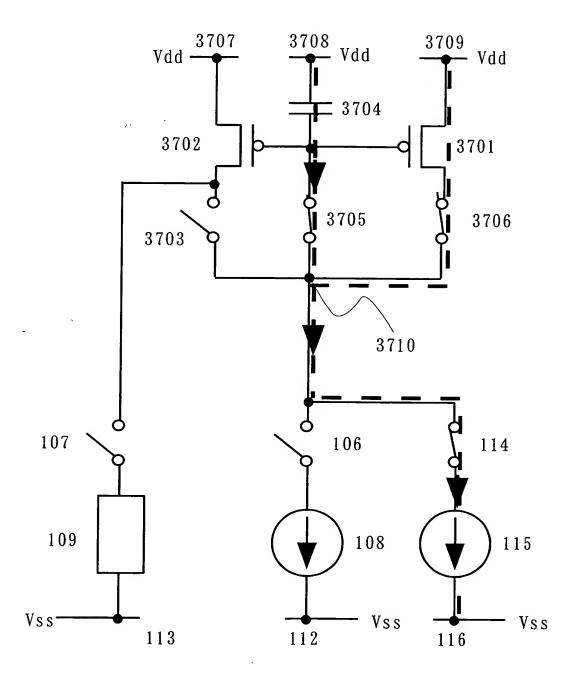


FIG. 48

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Appln No.: 10/787,347 Page Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

THEREOF

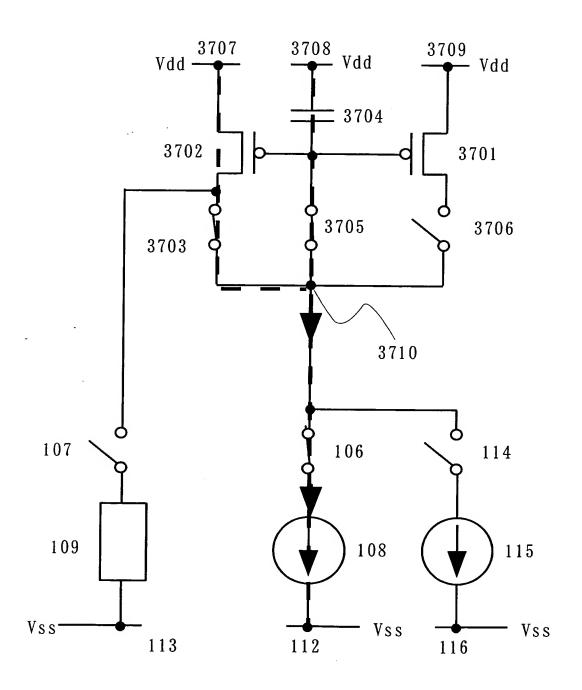


FIG. 49

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

THEREOF .

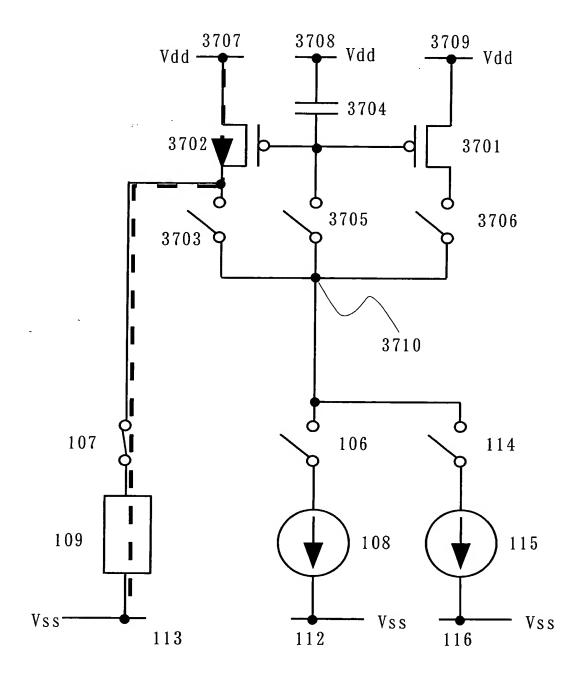


FIG. 50

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Appln No.: 10/787,347 Page & Applicant(s): Hajime Kimura SEMICONDUCTOR DEVICE AND DRIVING METHOD THEREOF

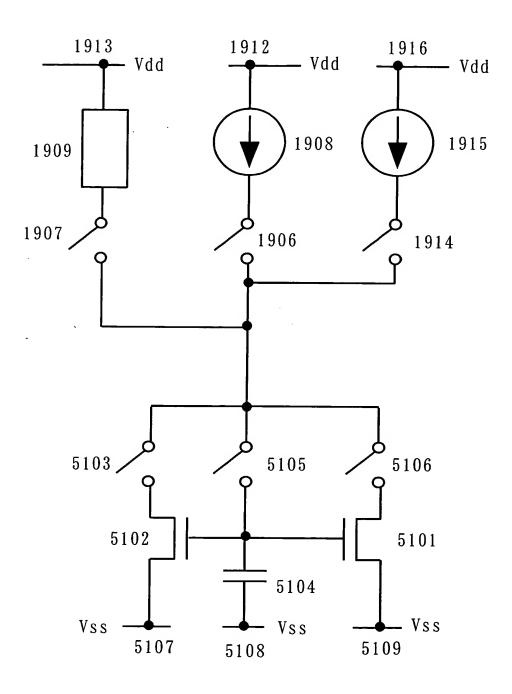


FIG. 51

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Appln No.: 10/787,347 Page 5
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

THEREOF

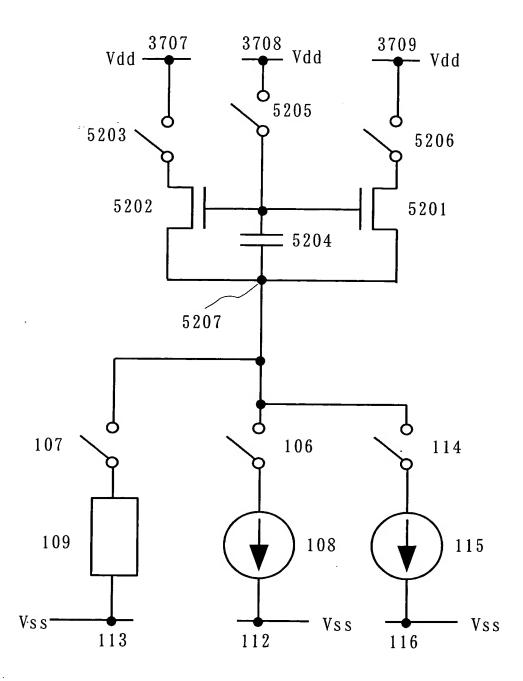


FIG. 52

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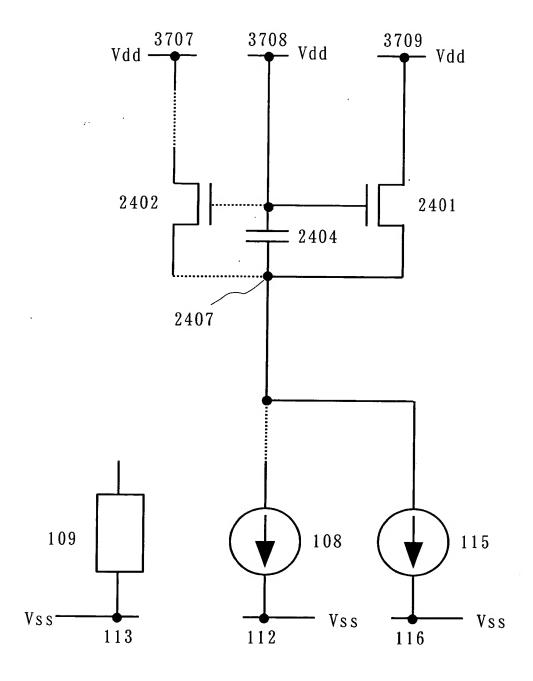


FIG. 53

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

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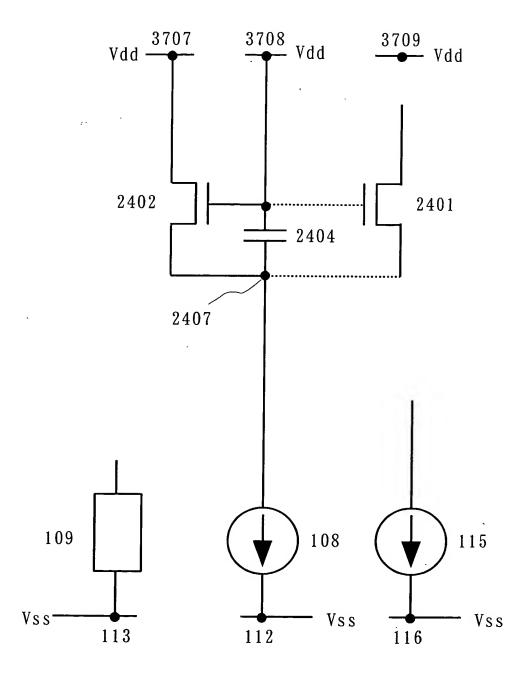


FIG. 54

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Applicant(s): Hajime Kimura
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THEREOF

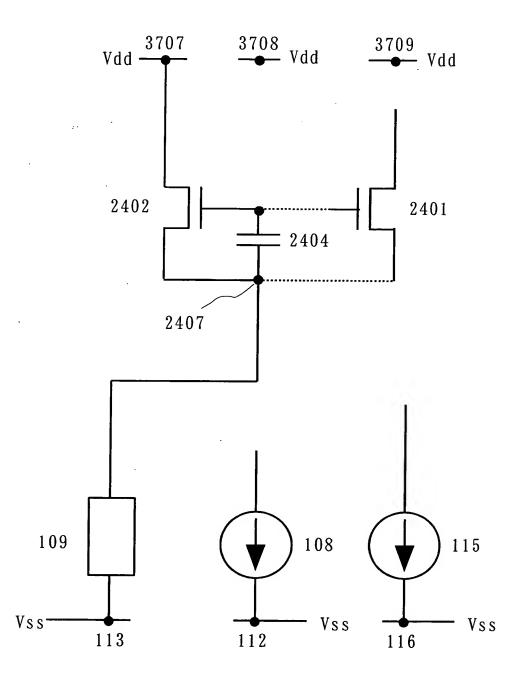


FIG. 55

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

**THEREOF** 

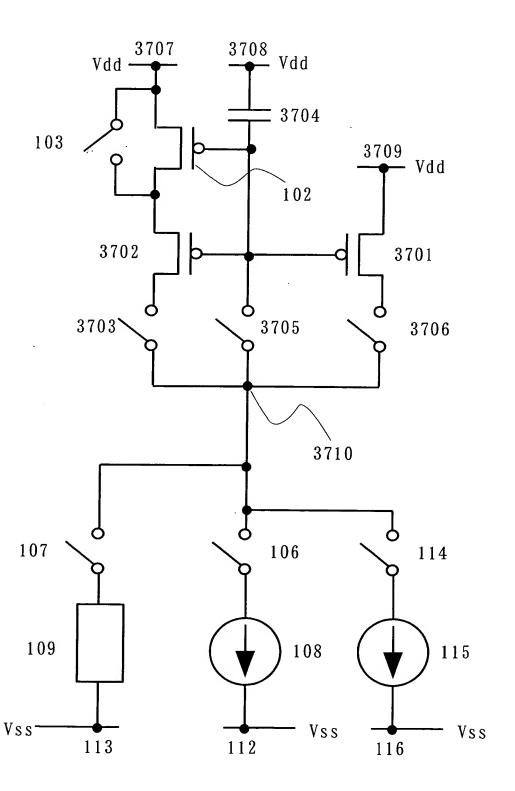


FIG. 56

SEMICONDUCTOR DEVICE AND DRIVING METHOD THEREOF

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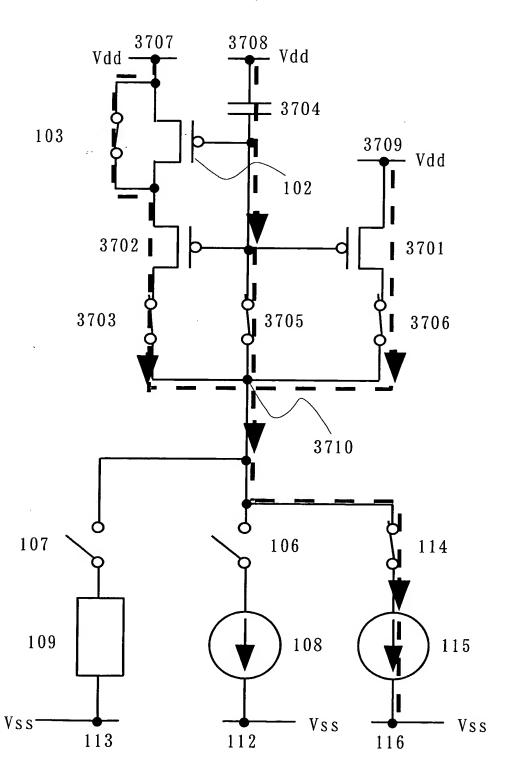


FIG. 57

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Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD Page 58 of 79

**THEREOF** 

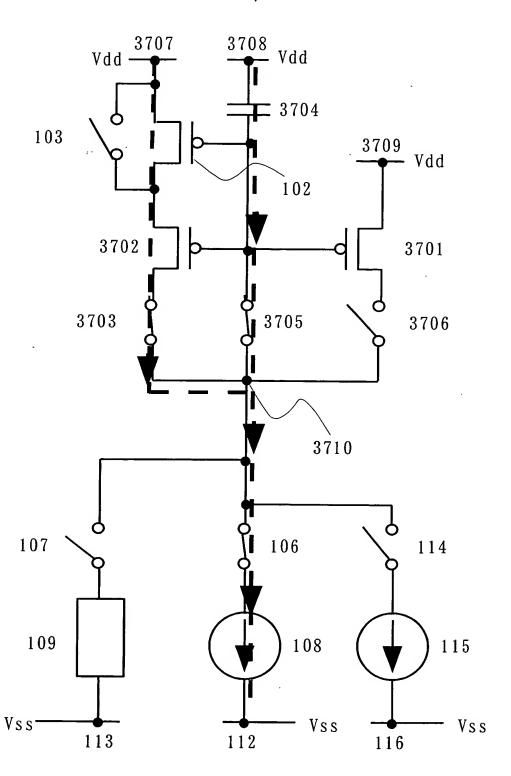


FIG. 58

Appln No.: 10/787,347 Page 5
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

**THEREOF** 

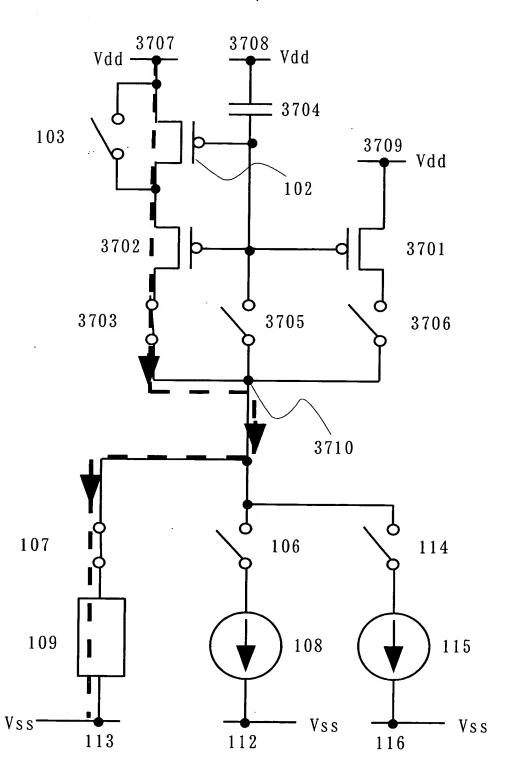


FIG. 59

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Appln No.: 10/787,347 Page 6
Applicant(s): Hajime Kimura
SEMICONDUCTOR DEVICE AND DRIVING METHOD

**THEREOF** 

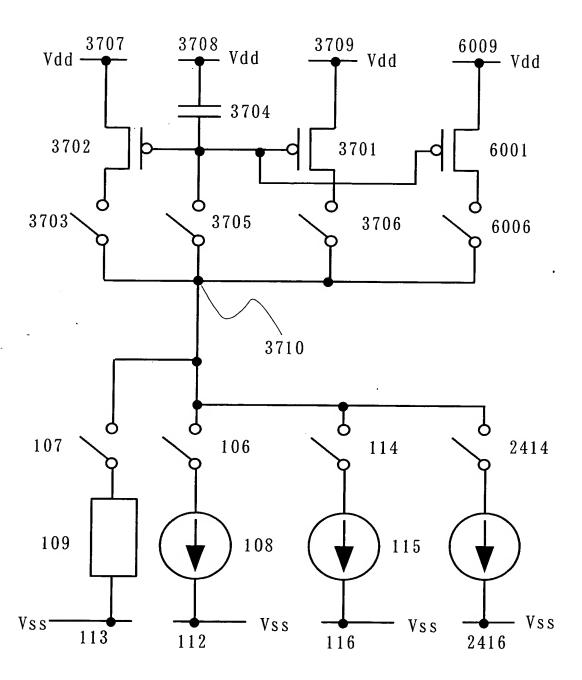


FIG. 60

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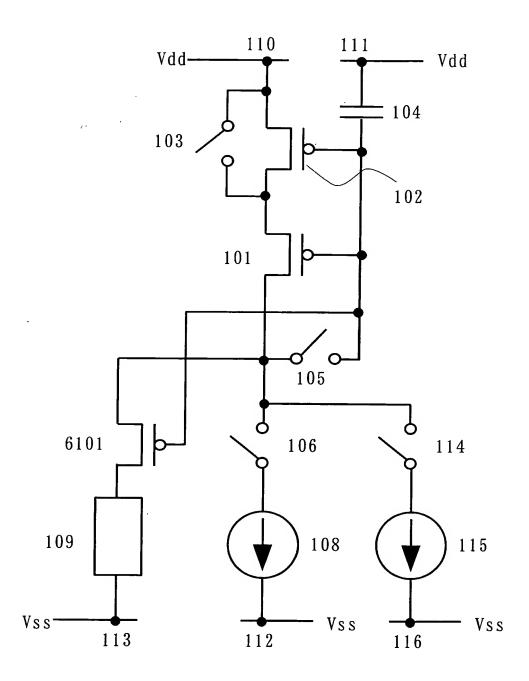


FIG. 61

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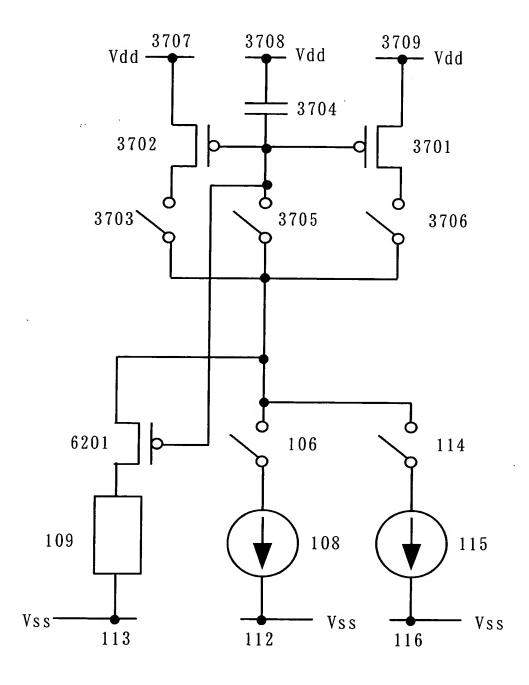


FIG. 62

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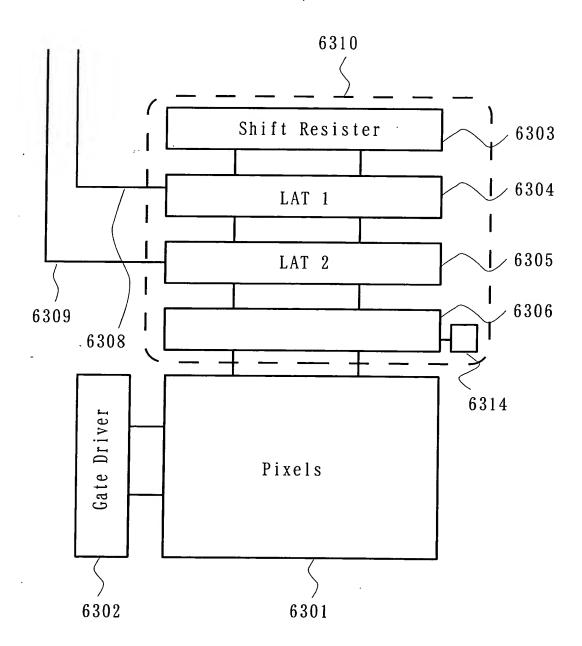


FIG. 63

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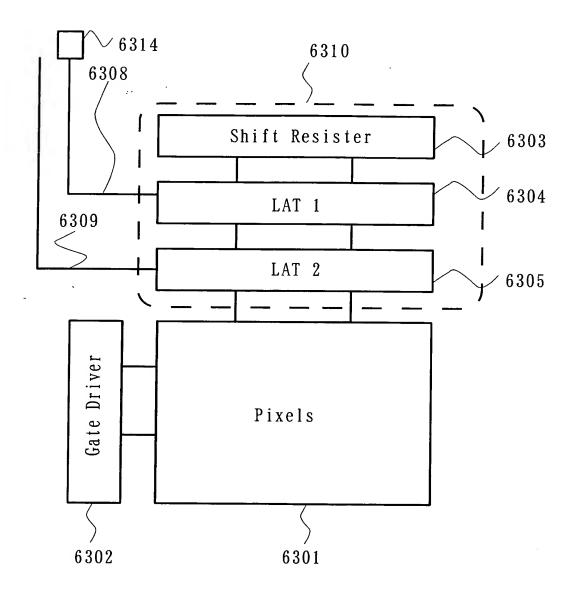
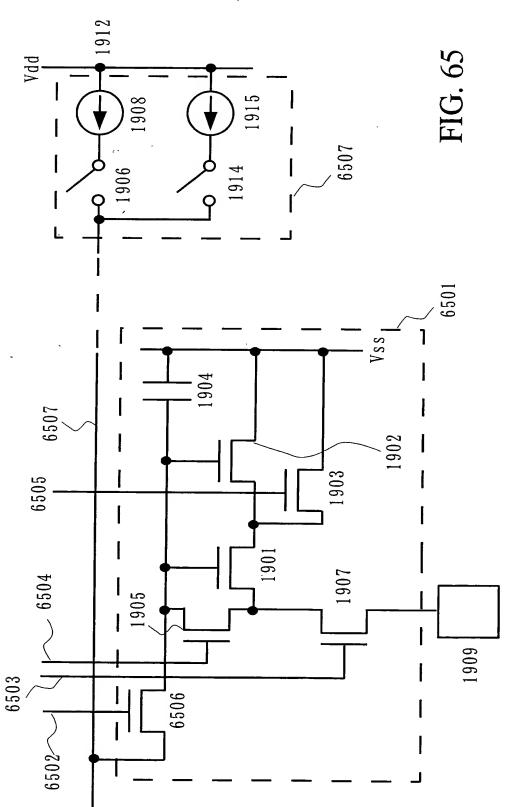


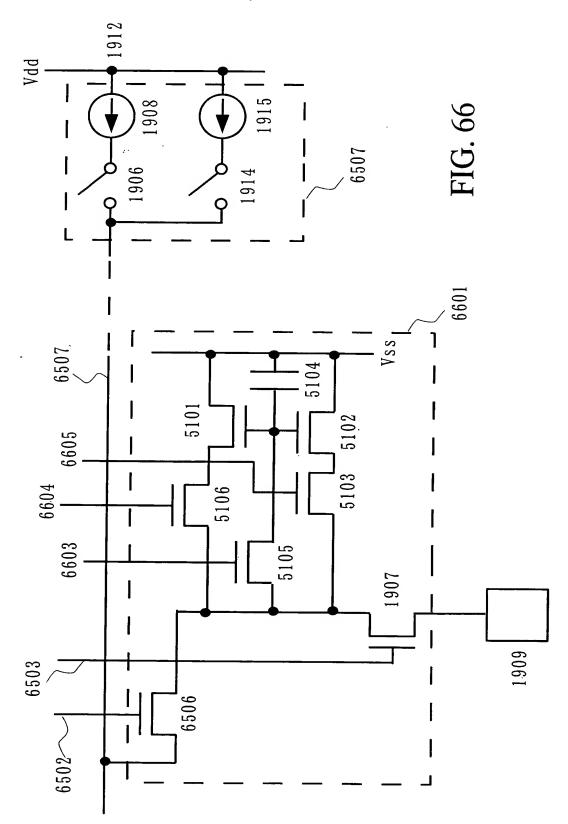
FIG. 64

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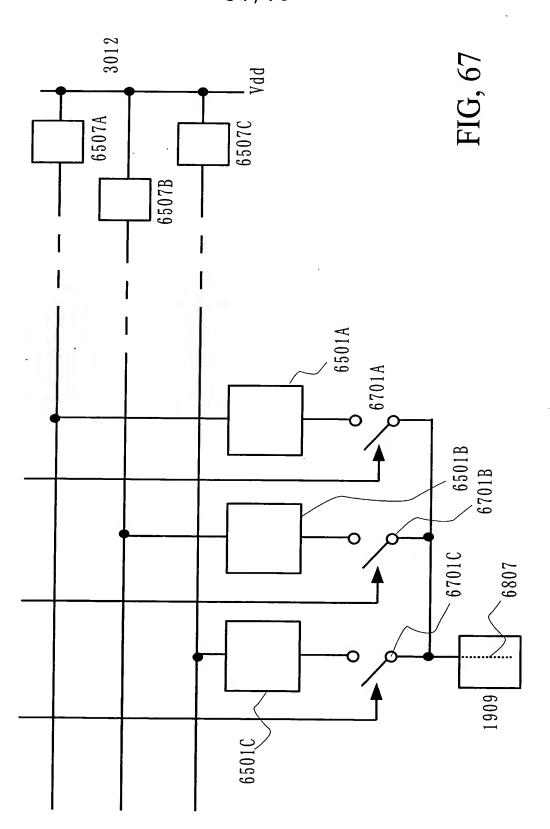
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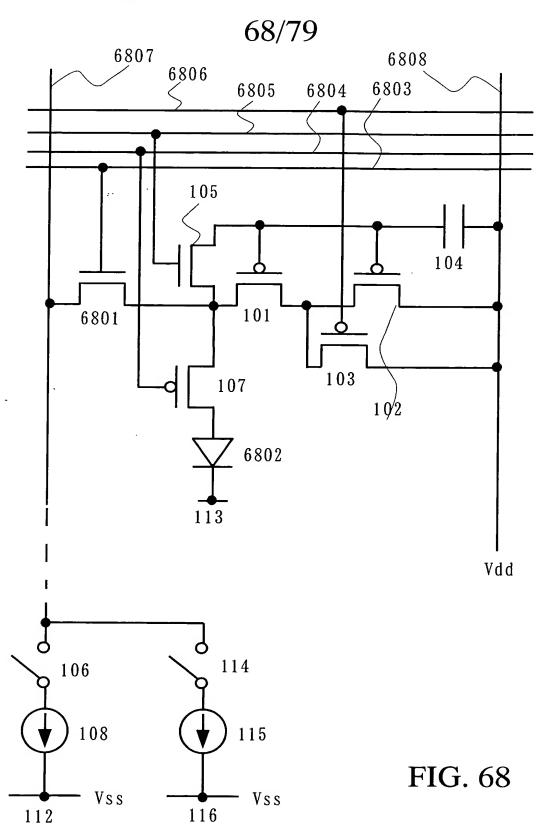
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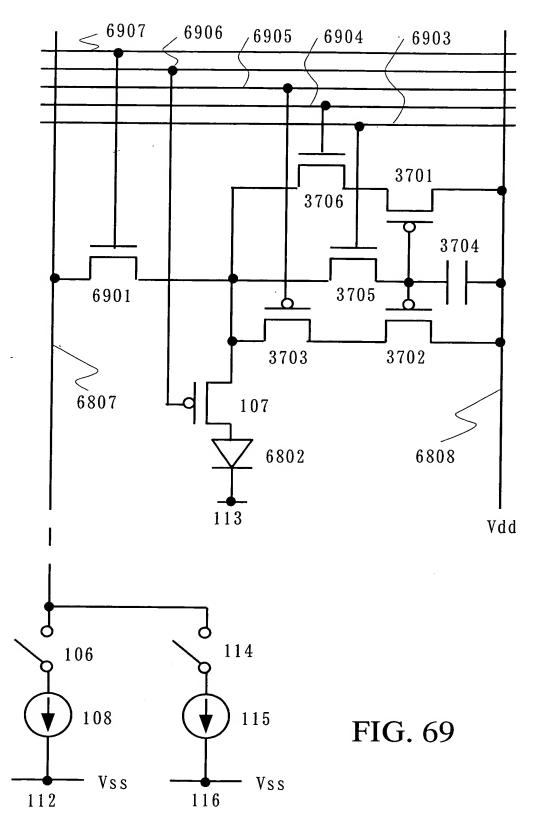
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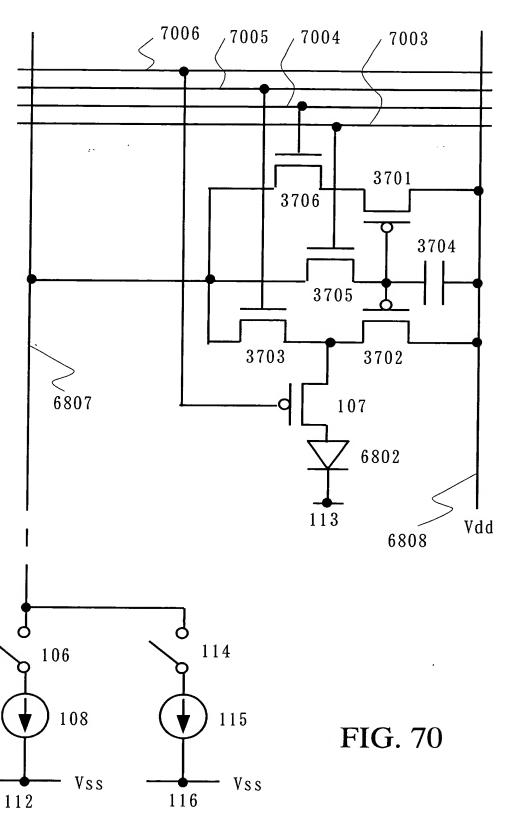
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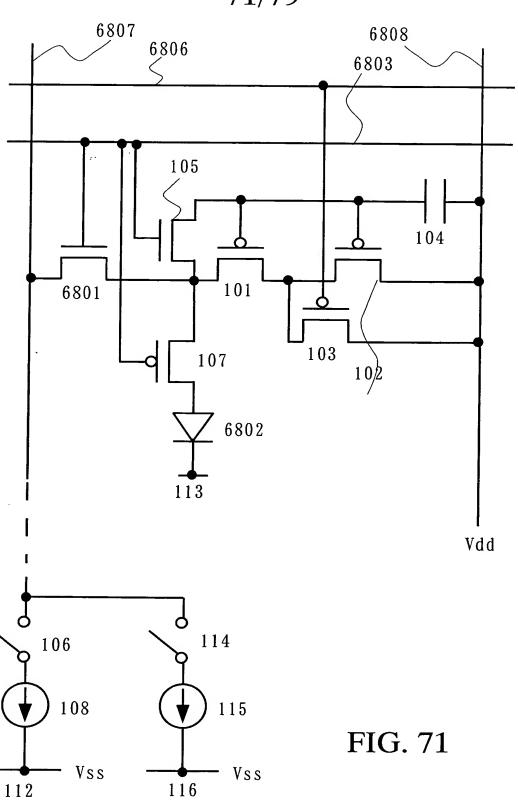
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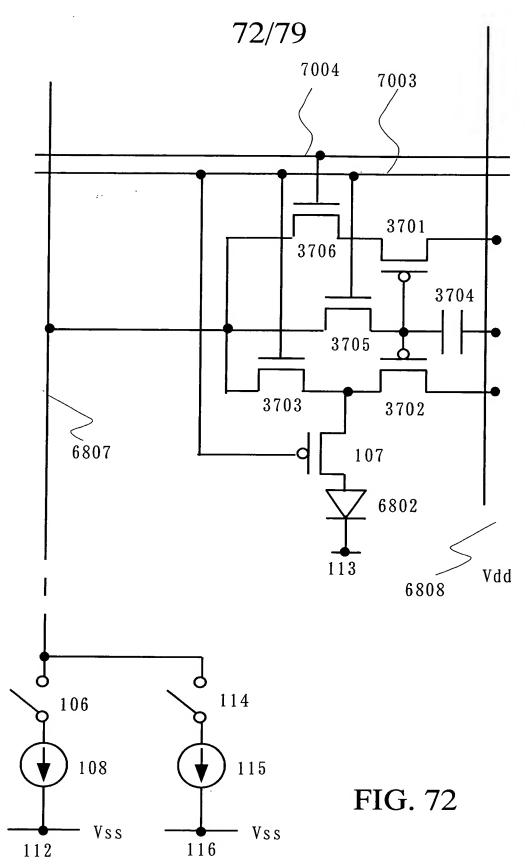






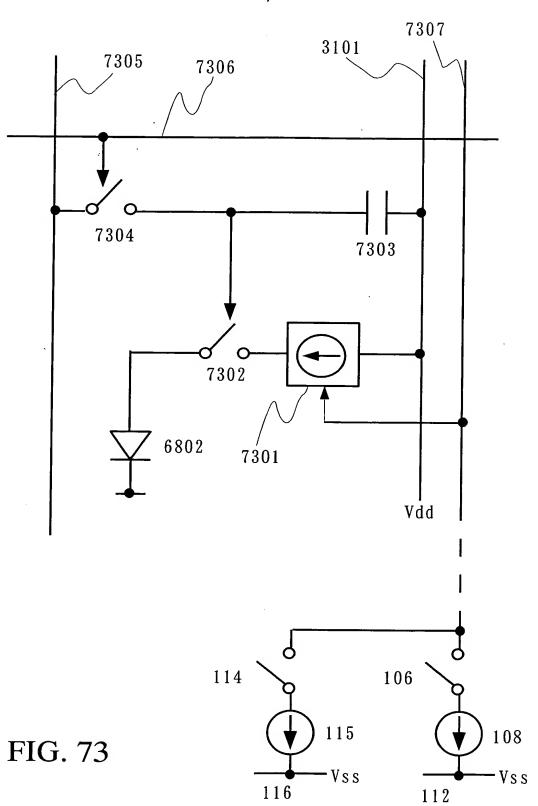
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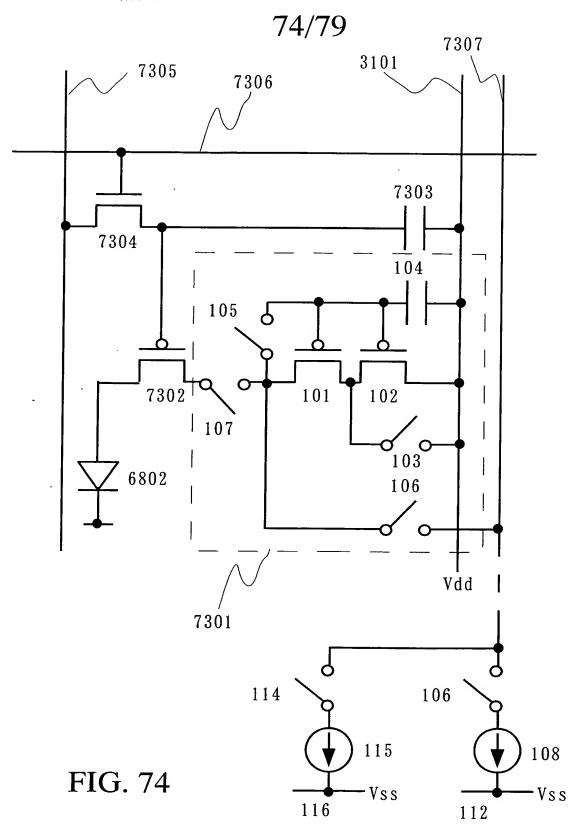
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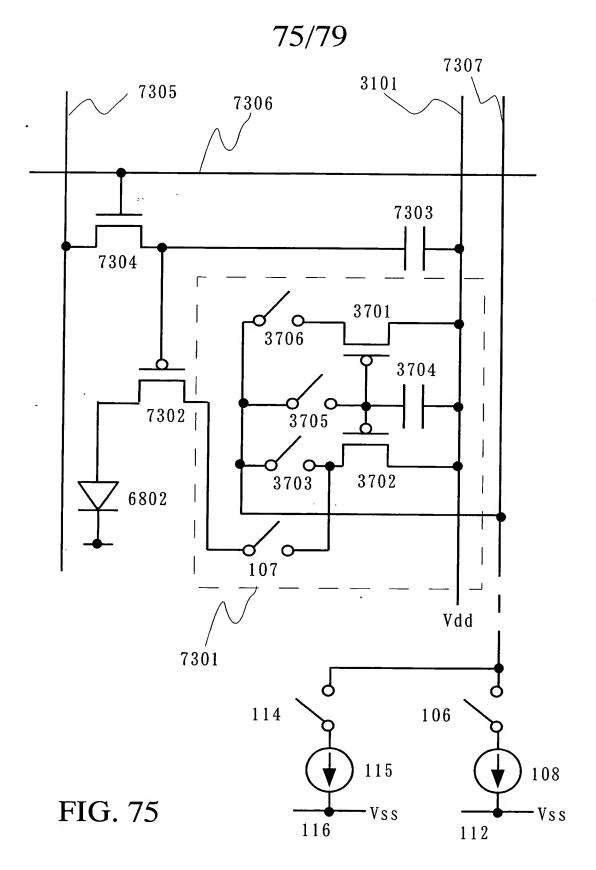
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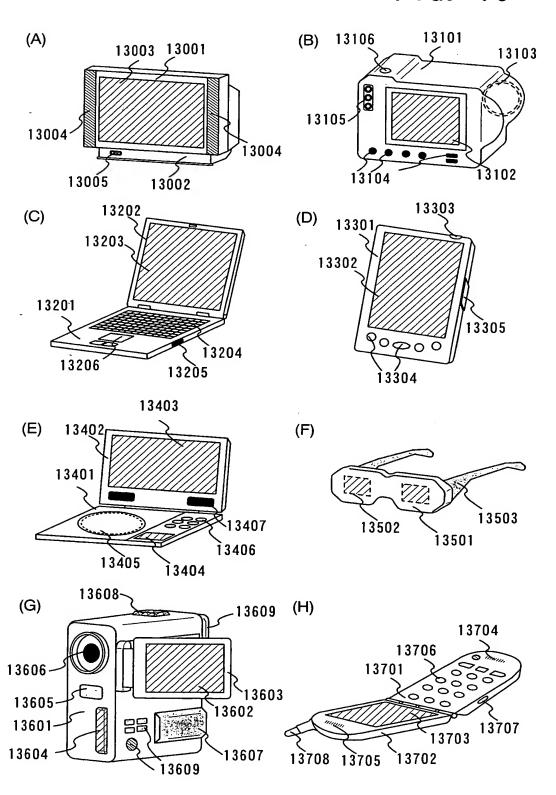
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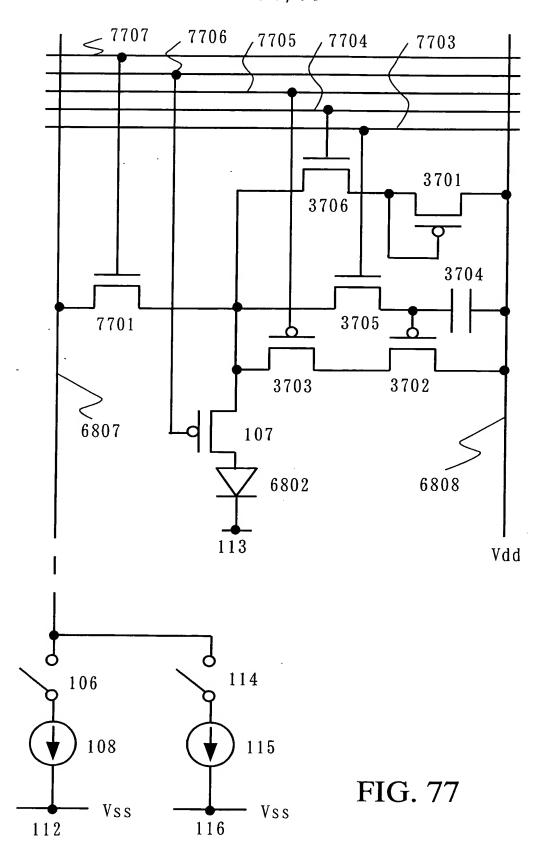
#### FIG. 76



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